

# Emitter common (dual digital transistors)

## EMA2 / UMA2N / FMA2A

●Features

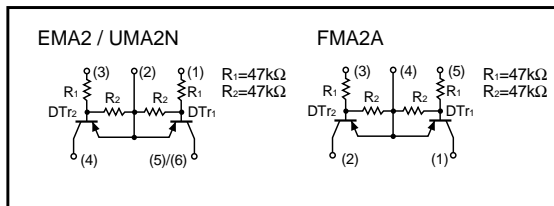
- 1) Two DTA144E transistors in a EMT or UMT or SMT package.
- 2) Mounting cost and area can be cut in half.

●Structure

Dual PNP silicon transistor (each with two built in resistors)

The following characteristics apply to both DTr1 and DTr2.

●Equivalent circuit

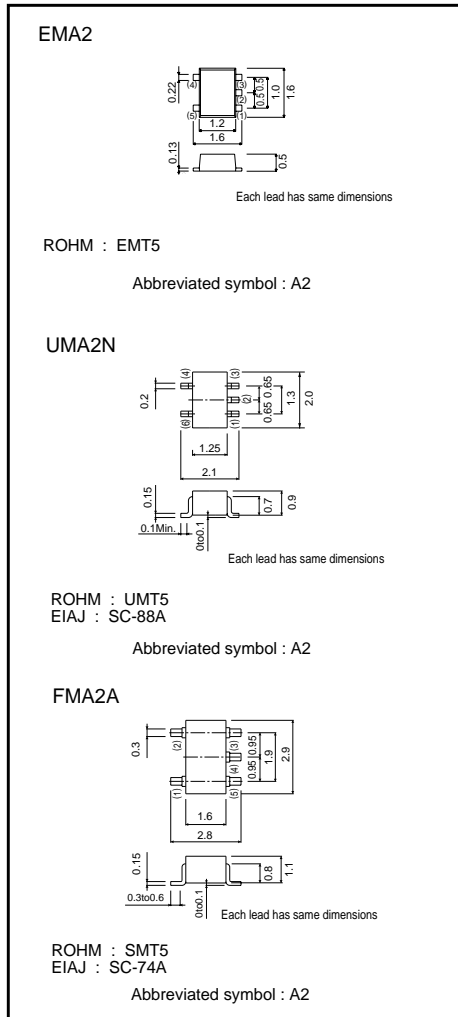


●Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-40 10	V
Output current	I <sub>o</sub>	-30	mA
	I <sub>C (Max.)</sub>	-100	
Power dissipation	EMA2, UMA2N	Pd	150 (TOTAL) mW *1
	FMA2A	Pd	300 (TOTAL) mW *2
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~+150	°C

\*1 120mW per element must not be exceeded.  
\*2 200mW per element must not be exceeded.

●External dimensions (Units : mm)



Transistors

●Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	-	-	-0.5	V	$V_{CC}=-5V, I_o=-100\mu A$
	$V_{I(on)}$	-3	-	-		$V_o=-0.3V, I_o=-5mA$
Output voltage	$V_{O(on)}$	-	-0.1	-0.3	V	$I_o/I_i=-5mA/0.25mA$
Input current	$I_i$	-	-	-0.18	mA	$V_i=-5V$
Output current	$I_{O(off)}$	-	-	-0.5	$\mu A$	$V_{CC}=-50V, V_i=0V$
DC current gain	$G_i$	68	-	-	-	$V_o=-5V, I_o=-10mA$
Transition frequency	$f_T$	-	250	-	MHz	$V_{CE}=10mA, I_E=-5mA, f=100MHz$ *
Input resistance	$R_1$	32.9	47	61.1	$k\Omega$	-
Resistance ratio	$R_2 / R_1$	0.8	1	1.2	-	-

\* Transition frequency of the device

●Packaging specifications

Type	Package	Taping		
	Code	T2R	TR	T148
	Basic ordering unit (pieces)	8000	3000	3000
EMA2	○	—	—	—
UMA2N	—	○	—	—
FMA2A	—	—	—	○

●Electrical characteristic curves

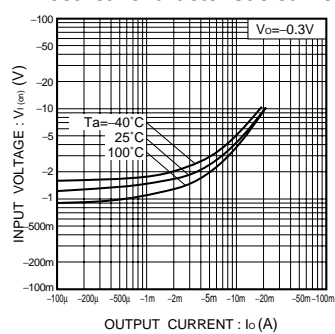


Fig.1 Input voltage vs. output current (ON characteristics)

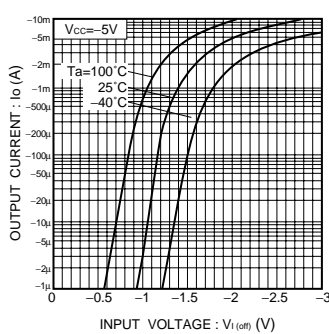


Fig.2 Output current vs. input voltage (OFF characteristics)

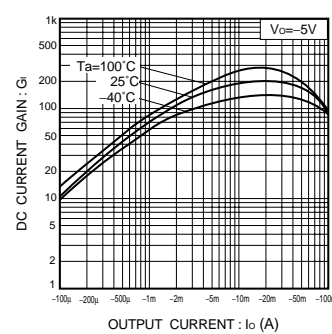


Fig.3 DC current gain vs. output current

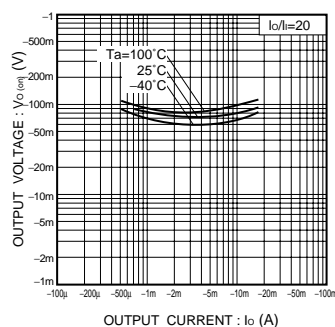


Fig.4 Output voltage vs. output current